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**INFORMATION DISCLOSURE
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Complete if Known

Application Number	08/903486
Filing Date	July 29, 1997
First Named Inventor	Forbes, Leonard
Group Art Unit	2811
Examiner Name	Mintel, W.

Sheet 1 of 1

Attorney Docket No: 00303.326US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
OHK	US-5698869	12/16/1997	Yoshimi, M., et al	257	192	09/13/1995
PHK	US-5886376	03/23/1999	Acovic, A., et al	257	316	07/01/1996
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PHK	US-6084248	07/04/2000	Inoue, S.	257	66	02/03/1998
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Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
PHK		BURNS, S.G., et al., In: Principles of Electronic Circuits, West Publishing Company, St. Paul, MN, (1987), pp.382-383	
PHK		WOLF, "Semiconductor Memory Process Integration", Silicon Processing for the VLSI Era Vol. 2: Process Integration, Lattice Press, California, (1990), pp.623-626	

EXAMINER

Donghee Kang

DATE CONSIDERED

5-24-02

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached